## EAST Search Hist ry

			EAST Searc	CII FI	156 17				
\	Hits	Sear	rch Query	DBs		Defaul Operat	٠ ١		Time Stamp
tef # _1	8368	((70 (70 (71 (71 (71	09/203) or (709/204) or 9/209) or (709/250) or 3/401) or (713/503) or 3/600) or (713/400) or 13/401) or (713/500) or 13/503) or (710/126) or 10/300)).CCLS.	USP EPO DER	AT; ); JPO; RWENT	OR			2006/09/13 20:58
L2	0	(Al ((r mi	LLEN and JAMES) and microprocessor or icro-processor) same (bus near gic) same (clock\$3 near5	EP	PAT; O; JPO; RWENT	OR		OFF	2006/09/13 21:07
L3	5:	1 (( m	(microprocessor or processor or hicro-processor) same (bus near ogic) same (clock\$3 near5 lelay\$3))	5 EF	SPAT; PO; JPO; ERWENT			ON	2006/09/13 21:01
L4		0 (1	MAYFIELD and MICHAEL and IOHN) and ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3))	E	SPAT; PO; JPO; ERWENT SPAT;	OR - OR	L	OFF	2006/09/13 21:02
L5			(NG and ALVAN and WING) and ((microprocessor or processor or micro-processor) same (bus neilogic) same (clock\$3 near5 delay\$3))	or     ar5	EPO; JPO DERWEN USPAT;	; T O	R	ON	2006/09/13 21:04
L6		363	(memory with control\$4) same ((transmit\$4 or send\$3 or forward\$3) near5 signal) L1 and L6		EPO; JPO DERWEN USPAT; EPO; JP	ŢT C	)R	ON	2006/09/13 21:05
L7		0	L6 same L3		USPAT; EPO; JF	NT CO;	OR	ON	2006/09/13 21:05
L9		11	L6 and L3		USPAT EPO; J DERWI	; P0;	OR	ON	2005/00/42 21:00
L1	.0	0	L9 and L1		USPAT EPO; 3 DERW	PO; ENT	OR	10	
L	11	11	((microprocessor or processor micro-processor) same (bus logic) same (clock\$3 near5 delay\$3) )same first same s	IICU. J		JPO;	OR	O	2000,007.20 =2.00

9/13/06 9:10:56 PM

## **EAST Search History**

		EAST Search	1113001 9			2.2.22
			USPAT; -	OR	ON	2006/09/13 21:08
.12 L13	34	micro-processor of processor of micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )same (first with (microprocessor or processor or micro-processor))  ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )and (first with (microprocessor or processor	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08
L14	27	micro-processor))and (second with (microprocessor or processor or micro-processor))  ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )and ((first with (microprocessor or processor or micro-processor))same (second	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08
L15	3	with (microprocessor or processor or micro-processor)))  ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )same ((first with (microprocessor or processor or micro-processor))same (second with (microprocessor or processor or micro-processor)))		OR	ON	2006/09/13 21:04

9/13/06 9:10:56 PM